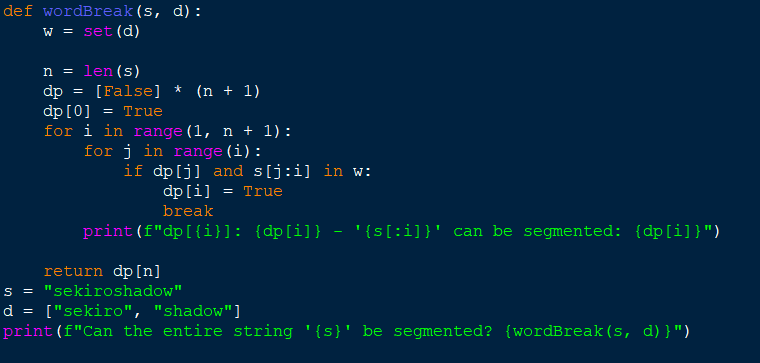
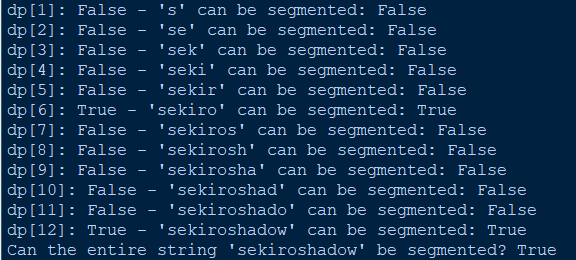
LAB 12

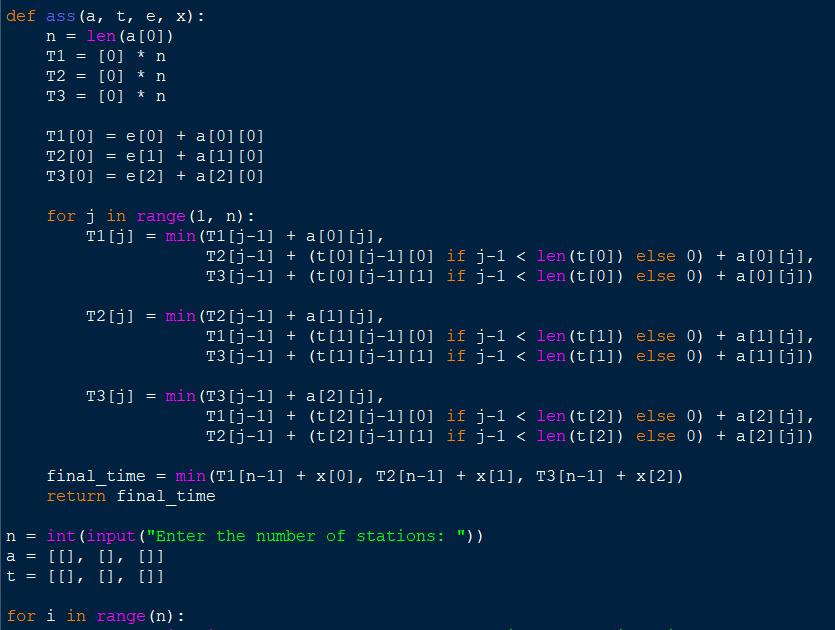
1.WORD BREAK

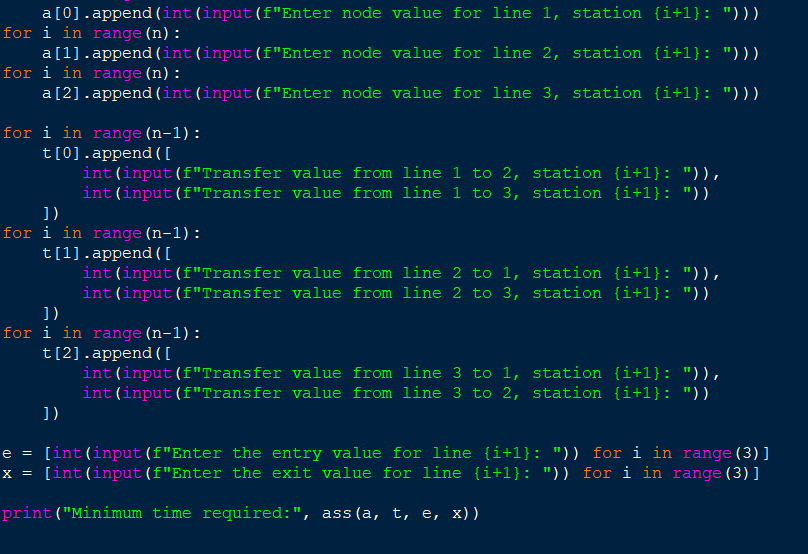


OUTPUT:

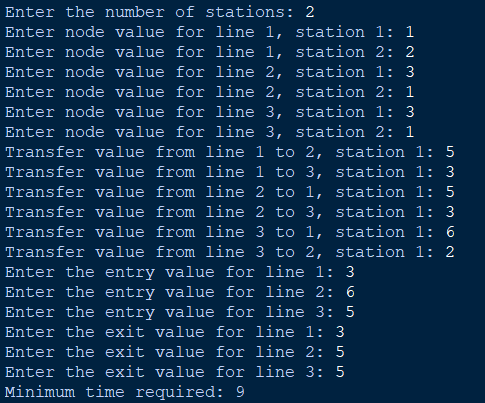


2.ASSEMBLY LINE SEHDDULING

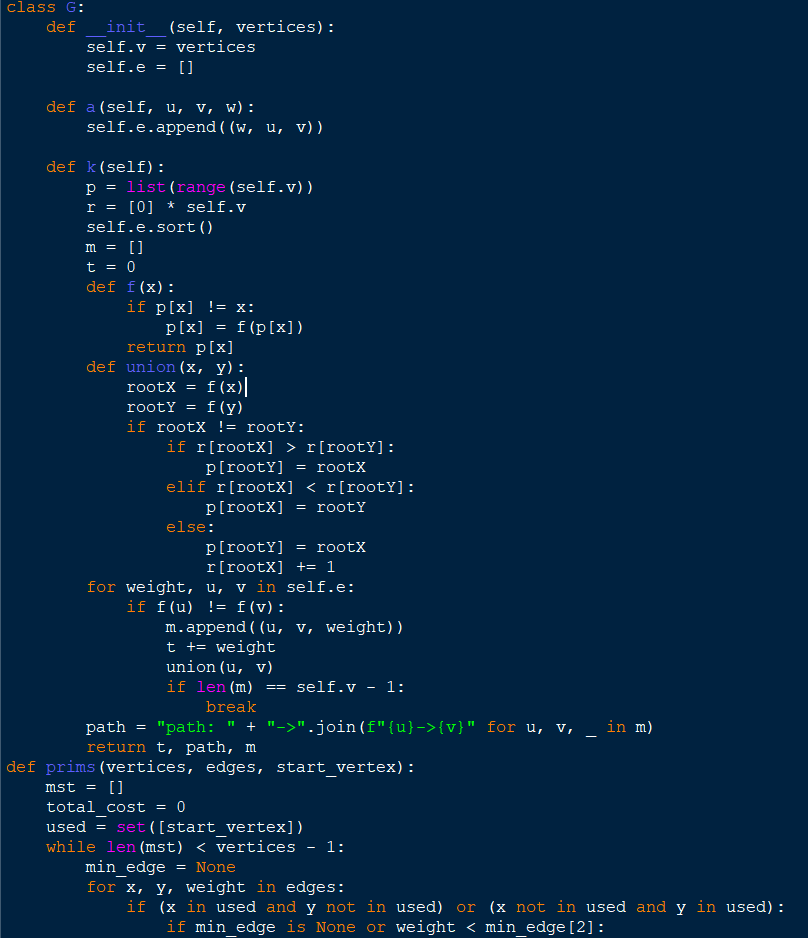


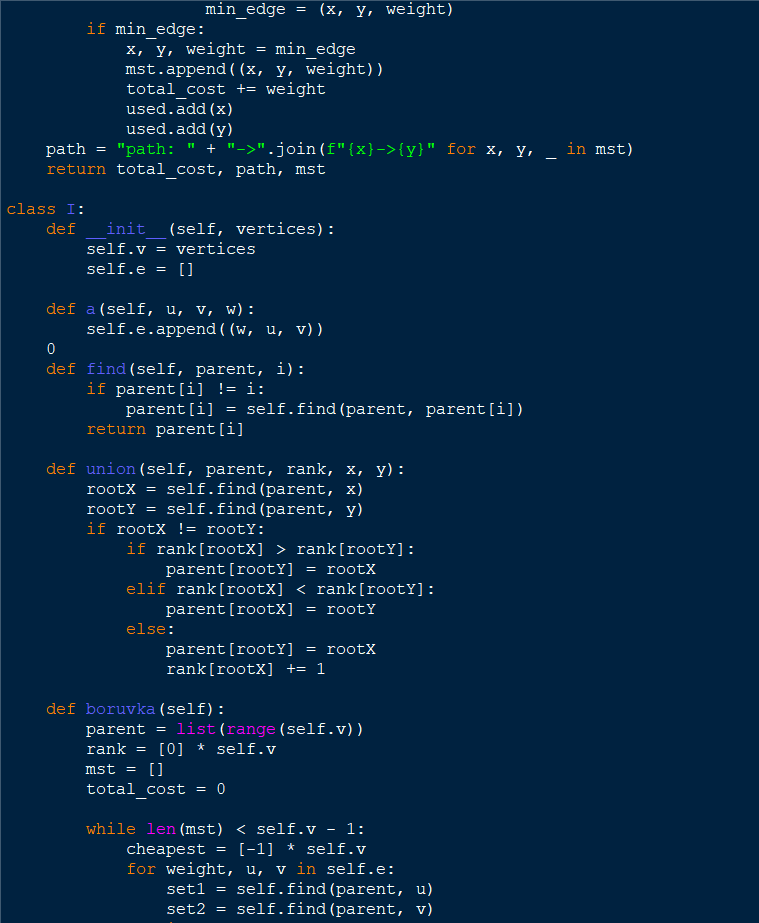


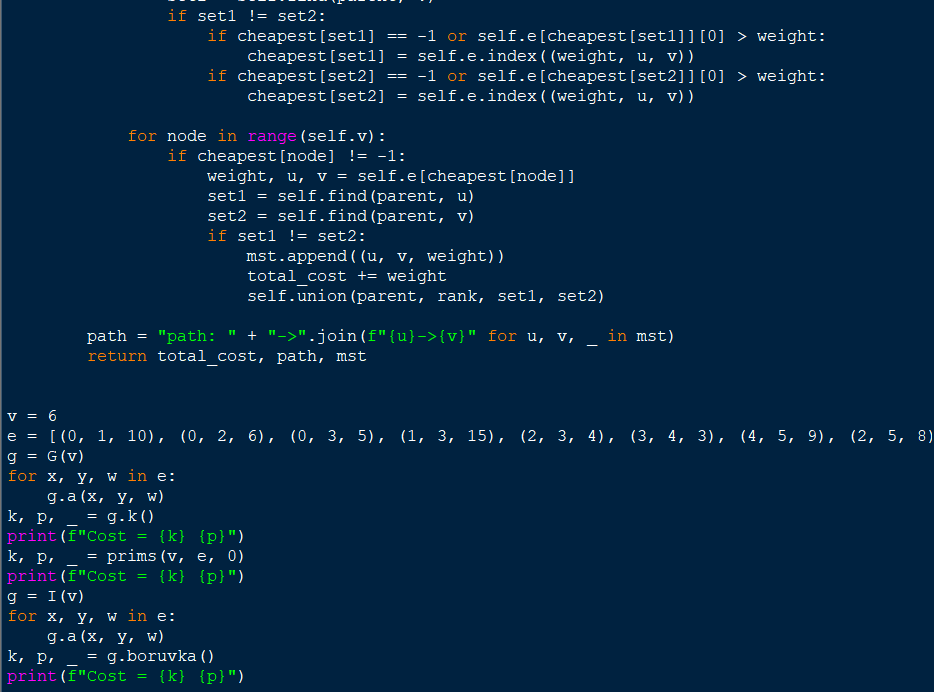
OUTPUT:



3.SHORTEST PATH COMBINATION







OUTPUT:

